

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. **UTILITY** Patent Application

O.I.P.E.

PATENT DATE

SCANNED

Q.A

APPLICATION NO. 09/923670	CONT/PRIOR F	CLASS 3812 257	SUBCLASS 499	ART. UNIT 2835	EXAMINER Thomas
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APPLICANTS

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title

Integrated circuit devices having dielectric regions protected with multi-layer insulation structures and methods of fabricating same

PTO-2040
12/99

ISSUING CLASSIFICATION

[illegible]

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.			NOTICE OF ALLOWANCE MAILED	
			ISSUE FEE	
			Amount Due	Date Paid
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